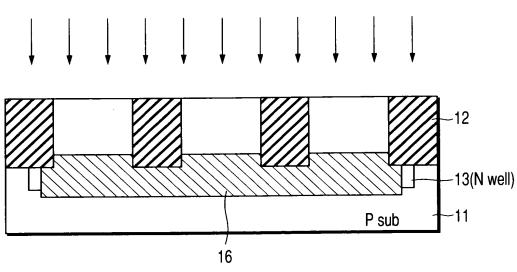


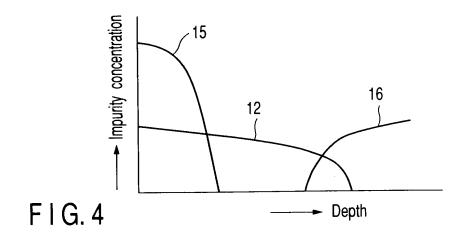
F I G. 2

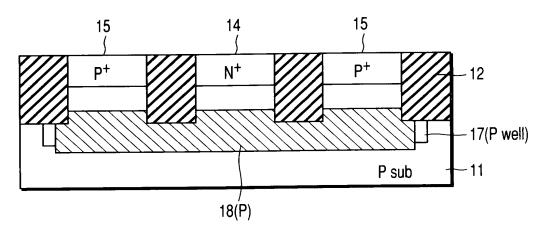


F I G. 3

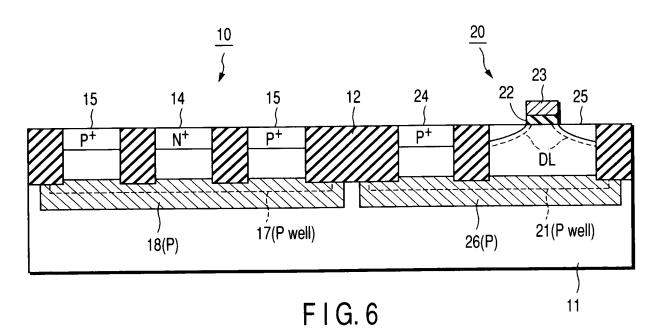


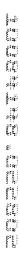
OBLON, SPIVAK, ET AL DOCKET #: 220199US2S INV: Tatsuya OHGURO SHEET _2_OF_7

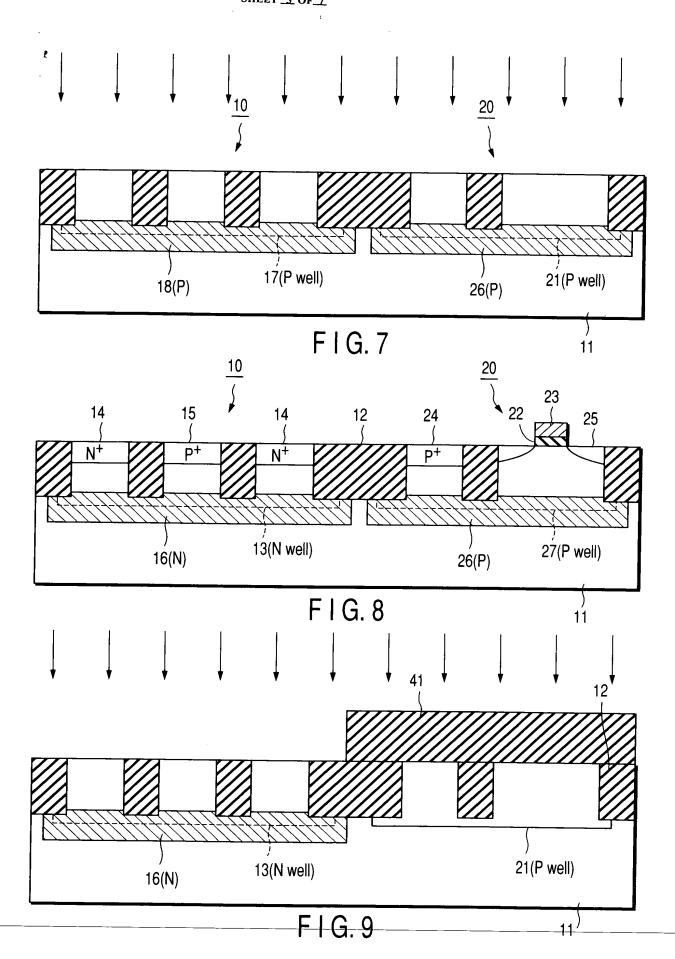




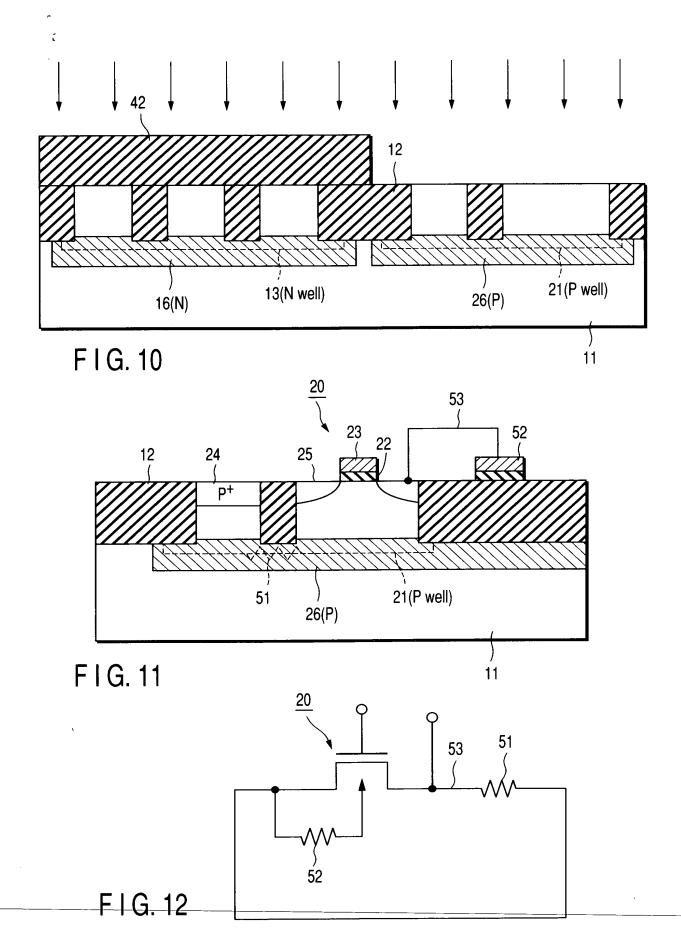
F I G. 5

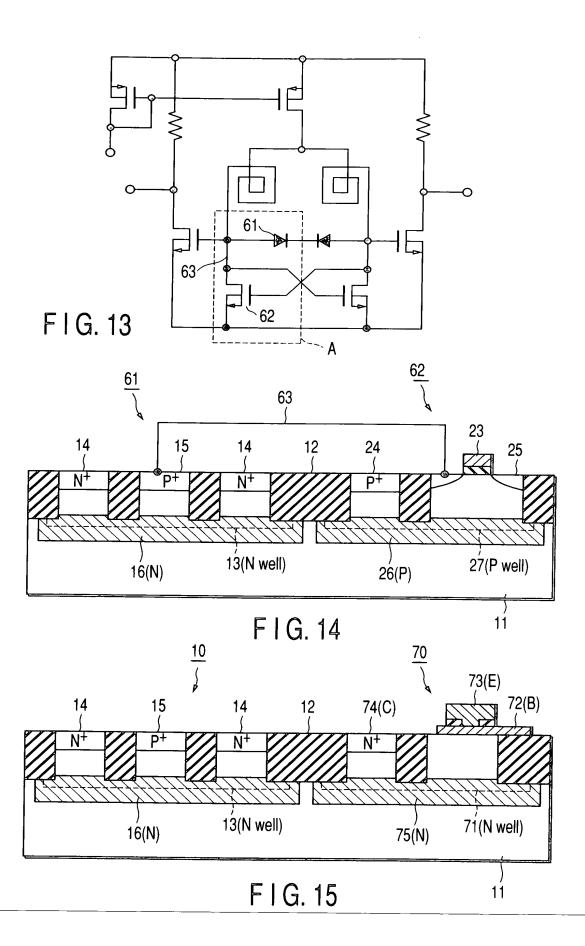






OBLON, SPIVAK, ET ALDOCKET #: 220199US2S INV: Tatsuya OHGURO SHEET 4 OF 7





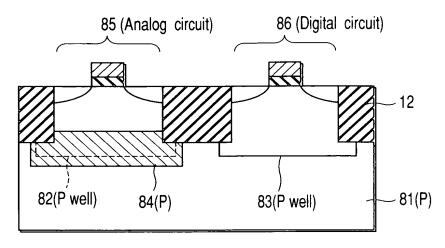


FIG. 16

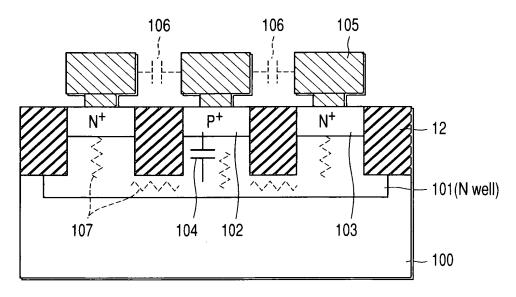
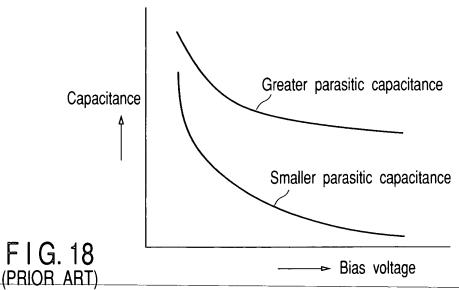
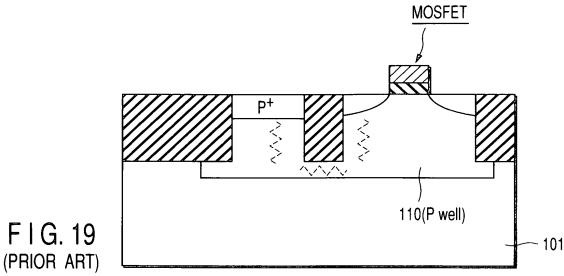
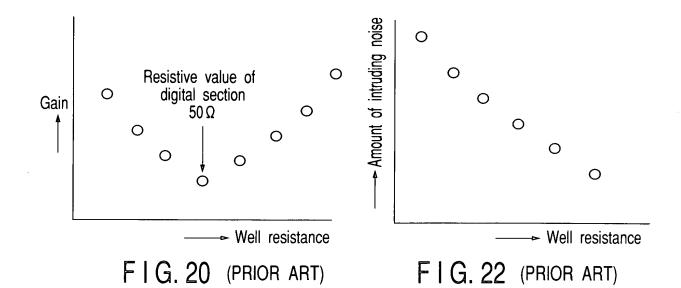


FIG. 17 (PRIOR ART)



THE REPORT OF THE PARTY OF THE





F I G. 21
(PRIOR ART)

Analog circuit

Digital circuit

122(P well)

120 (P+Substrate)